CURRICULUM VITAE

***Renuka V Tali***

#201, Murari Orchids

Srinivasapura,

11th Block, Banashankari 6th stage

Bengaluru, 560060

**E-mail:** [renukavtali@gmail.com](mailto:renukavtali@gmail.com)

**Mob:** 9110407827

## 

**CAREER OBJECTIVE:**

Pursue a challenging career in the field of **Design Verification (DV)** leading to professional as well as personal growth and to contribute to organization by utilizing my skills, knowledge, and abilities.

**HIGHLIGHTS:**

* Knowledge of Verilog, System Verilog and UVM Concepts.
* Knowledge of Digital System Design, Microprocessors, Microcontroller Image Processing, Machine Learning, Operating System, Wireless and Mobile Communication and C/C++.

**RESEARCH ACTIVITY:**

* Pursuing Ph.D. in Computer Science (Status: Thesis Preparation)
* Three Research articles published in Scopus and WOS free Journals.

**TRAINING AND CERTIFICATION:**

Trained and certified in DV from the Chip Edge Technologies Pvt. Ltd Bangalore.

Able to develop

* Verification methodology and test-plan for new design
* APB protocol SV test bench
* Coverage-driven verification test plans
* Constrained randomization based methodologies
* Test specifications (plans) and create directed and random test cases

# PROFESSIONAL PROJECT:

# Project: Built SV Test bench for Adder, Memory and APB Communication Protocol.

**TECHNICAL SKILLS:**

Sound technical knowledge of design tools

* Synopsys (VCS, GVIM)
* Cadence
* Xilinx ISE
* Matlab and Multisim

***Operating system***

MS Windows and Linux.

# WORK EXPERIENCE: 12 years of Academic Experience (6 years of Research)

1. Currently working as Assistant professor in **“K. S. School of Engineering and Management, Bangalore, Karnataka”** from 15th July 2013 till date.
2. Worked as Assistant professor in **“Global Academy of Technology, Bangalore, Karnataka”,** for one year in 2010.
3. Worked as Lecturer in **“Tontadarya College of Engineering, Gadag, Karnataka”** from 5th September 2005 to 9th September 2007.

# SUBJECT HANDLED:

# Microprocessors (8085, 8086) Microcontroller (8051), Digital System Design, Wireless and Satellite Communication, Image Processing, Operating System, Real time systems, Digital Electronics Lab, Verilog HDL Lab and Microcontroller Lab.

**EDUCATIONAL QUALIFICATION:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Course** | **Name of the Institution** | **University** | **Year of Passing** | **Aggregate** |
| **Ph.D.** | KSIT Research Centre | Visvesvaraya Technological University (VTU), Belagavi | Pursuing | **-** |
| **M.Tech**  [Digital Communication] | BMSCE, Bangalore | 2012 | **80.2**% |
| **BE**  [E&CE] | PDIT, Hospet | 2005 | **72.7**% |

**PERSONAL PROFILE:**

Name : Renuka V Tali

Date of Birth : 29-06-1982

Marital Status : Married

Spouse : Prof. Dr. Siddalingesh Bandi, GAT

Languages Known : English, Kannada, and Hindi

**DECLARATION:**

I hereby solemnly affirm that all that statements made in the curriculum vitae are true and correct to the best of my knowledge and belief.

Place: Bangalore Renuka V Tali

Date:25/03/2023